

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Stephen M. Gates, et al.

Examiner: Unassigned

Serial No.: To be assigned

Art Unit: Unassigned

Filed: Herewith

Docket: YOR920010052US3 (16876Z)

For: ELECTRONIC STRUCTURES WITH
REDUCED CAPACITANCE

Dated: January 27, 2004

Commissioner for Patents
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

In connection with filing the above-identified patent application under 37 C.F.R. §1.53(b), applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

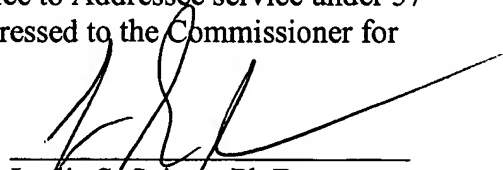
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I hereby certify that this correspondence is being deposited with the United States Postal Service Express Mail Post Office to Addressee service under 37 C.F.R. §1.10 on the date indicated above and is addressed to the Commissioner for Patents, Box 1450, Alexandria, VA 22313-1450.

Dated: January 27, 2004



Leslie S. Szivos, Ph.D.